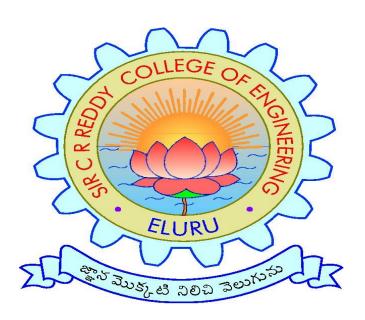
# SIR C.R.REDDY COLLEGE OF ENGINEERING, ELURU DEPARTMENT OF INFORMATION TECHNOLOGY COURSE HANDOUT



SUBJECT: FUNDAMENTALS OF MICROPROCESSORS AND MICROCONTROLLERS

CLASS: III/IV B.Tech., II SEMESTER, A.Y.2022-23
INSTRUCTOR: Smt. T.SATYA NAGAMANI

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#### **COLLEGE VISION**

To emerge as a premier institution in the field of technical education and research in the state and as a home for holistic development of the students and contribute to the advancement of society and the region.

### **COLLEGE MISSION**

To provide high quality technical education through a creative balance of academic and industry oriented learning; to create an inspiring environment of scholarship and research; to instill high levels of academic and professional discipline; and to establish standards that inculcate ethical and moral values that contribute to growth in career and development of society in general.

# **VISION OF THE DEPARTMENT**

To be a premier Department in the region in the field of Information Technology through academic excellence and research that enable graduates to meet the challenges of industry and society

# MISSIONOF THE DEPARTMENT

- ❖ To Provide dynamic teaching-learning environment to make the students industry ready and advancement in career;
- ❖ To inculcate professional and leadership quality for better employability and entrepreneurship;
- ❖ To make high quality professional with moral and ethical values suitable for industry and society

# PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

**PEO1:** Solve real world problems through effective professional skills in Information Technology industry and academic research.

**PEO2:** Analyze and develop applications in Information Technology domain and adapt to changing technology trends with continuous learning.

**PEO3:** Practice the profession in society with ethical and moral values.

# **PROGRAM OUTCOMES (POs)**

**PO1: Engineering Knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.

**PO2: Problem Analysis:** Identify, formulate, research literature, and analyze complex engineering problems reaching substantiated conclusions using the first principles of mathematics, natural sciences, and engineering sciences.

**PO3: Design/Development of Solutions:** Design solutions for complex engineering problems and system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, society, and environmental considerations.

**PO4: Conduct Investigations of Complex Problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

**PO5: Modern Tool Usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.

**PO6:** The Engineer and Society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.

**PO7: Environment and Sustainability:** Understand the impact of the professional engineering solutions in society and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.

**PO8: Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.

**PO9: Individual and Team Work**: Function effectively as an individual, and as a member or leader in diverse teams, and in multi-disciplinary settings.

**PO10: Communication**: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO11: Project Management and Finance: Demonstrate knowledge and understanding of the

engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multi-disciplinary environments.

PO12: Life-long Learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

# PROGRAM SPECIFIC OUTCOMES (PSOs)

**PSO1:** Design and develop software in the area of relevance under realistic constraints.

**PSO2:** Adapt new and fast emerging technologies in the field of Information Technology.

## JNTUK ACADEMIC CALENDAR

Website: www.jntuk.edu.in Email: dap.a jntuk.edu.in



Phone: 0884-2300991

Directorate of Academic Planning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA-533003, Andhra Pradesh, INDIA

(Established by AP Government Act No. 30 of 2008)

Lr. No. DAP/AC/III Year /B. Tech/B. Pharmacy/2022

Data

Date 14.09.2022 Dr. KVSG Murali Krishna, M.E. Ph.D.

Director, Academic Planning JNTUK, Kakinada

All the Principals of Affiliated Colleges, JNTUK, Kakinada.

Academic Calendar for III Year - B. Tech/B. Pharmacy for the AY 2022-23

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TER		
From	To	Weeks
15.07.2022	30.07.2022	2W
01.08.2022	24.09.2022	8W
26.09.2022	01.10.2022	1 W
03.10.2022	26.11.2022	8W
28.11.2022	03.12.2022	1 W
05.12.2022	10.12.2022	1 W
12.12.2022	25.12.2022	2W
02.01.2023		
TER		
02.01.2023	25.02.2023	8W
27.02.2023	04.03.2023	1 W
06.03.2023	29.04.2023	8W
01.05.2023	06.05.2023	1.00
08.05.2023	13.05.2023	1W
15.05.2023	27.05.2023	2W
	FER From 15.07.2022 01.08.2022 26.09.2022 03.10.2022 28.11.2022 05.12.2022 12.12.2022 02.01.2023 FER 02.01.2023 06.03.2023 01.05.2023 08.05.2023	Tex   To   To   15.07.2022   30.07.2022   01.08.2022   24.09.2022   26.09.2022   01.10.2022   03.10.2022   28.11.2022   28.11.2022   26.11.2022   10.12.2022   12.12.2022   12.12.2022   10.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   12.12.2022   10.12.2023   06.03.2023   06.03.2023   06.03.2023   06.03.2023   06.05.2023   06.05.2023   08.05.2023   13.05.2023   08.05.2023   13.05.2023   08.05.2023   13.05.2023   08.05.2023   08.05.2023   13.05.2023   08.05.2023   08.05.2023   13.05.2023   08.05.2023   08.05.2023   13.05.2023   08.05.2023

\* As per the APSCHE Guidelines Out of the Total 180 hours of Community Service Project leading to 4 Credits, two weeks will be offline and remaining project work can be done during the III-I semester weekends and holidays. The summer internship can be done in online cum offline during III-I and III-II semesters.

Academics & Planning, JNTUK
Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK Academic Planning
Copy to Rector, Registrar, JNTUK

Copy to Rector, Registrar, JNTUK Copy to Director Academic Audit, JNTUK Copy to Director of Evaluation, JNTUK

#### **Department Academic Calendar**

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Jul 22						1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	
Aug 22		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31					
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Nov 22			1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	L	L			
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Apr 23							1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	Ш
May 23		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	L				Ш
Jun 23					1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30			
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# **Course Description**

This is a graduate course surveying the concepts of fundamentals of microprocessors and micro controllers. It covers topics of 8085 microprocessor, 8086 architecture and family and its programming constructs. It includes advanced topics like interfacing with 8086 with peripheral interfaces like 8255, 8251, 8237a and 8259 along with A/D & D/A converters.

This course also emphasis on advanced processors called microcontrollers in real time applications, their architecture, and programming concepts with 8051. It also gives practice with case study on simple programming exercises, keyboard interface, display interface and controlling applications like stepper motor control, automation systems.

# **Course Objectives**

Upon completion of this course, students will be able to do the following:

- Get aware of concepts of microprocessors, microcontrollers, architectures, their organization and programming concepts.
- Able to use microcontrollers in real time applications along with microprocessors.
- Get the knowledge of interfacing with various real time devices in various applications.
- Can compare the performance of microprocessors and microcontrollers in interface design of controller based systems.

# **Course Outcomes**

Students are able to

CO No's	Cos	Level
CO1	Will be able to understand various microprocessors, their architectures, families, assembly language programming concepts, interfacing with other peripheral interfacing chips.	L2
CO2	Will be able to understand various microcontrollers, memory organization, their architectures, families, programming concepts, interfacing, control algorithms.	L2
CO3	Will be able to demonstrate various programming techniques of microprocessors and microcontrollers and interface programming with peripherals.	L3
CO4	Will be able to Analyze the performance of microprocessors, microcontrollers, interfacing techniques in designing processor/controller based systems.	L4

S.No	Unit	Description	Teaching Aids	co
1.	I	Introduction to 8085 microprocessor	BB	CO1
2.		Architecture of 8085	BB	CO1
3.		Memory organization	BB	CO1
4.		I/O ports	BB	CO1
5.		Interrupts of 8085	BB	CO1
6.		Pin diagram of 8086	BB	CO1
7.		Family of 8086	BB	CO1
8.		, internal architecture of 8086	BB	CO1
9.		Interrupts, 8086 timing	BB/PPT	CO1
10.		Minimum and maximum configuration modes	PPT	CO1
11.	II	Programming in 8086 steps	PPT	CO1

Addressing modes, assembler directives  BB CO1  14. Program writing constructs  BB CO1  15. Simple programs with assembler  BB CO1/CO3  16. Assembly language development tools  BB CO1/CO3  17. III 8086 interfacing, semiconductor memories  BB CO1/CO3  18. Memory interfacing  BB CO1/CO3  19. 8255 programmable peripheral interface  BB/PPT CO1/CO3  20. Interfacing switches and LEDS  BB CO1/CO3  21. Seven segment displays  BB/PPT CO1/CO3  22. Software and hardware interrupt applications  BB CO1  23. 8251 USART architecture  BB/PPT CO1  24. 8251 interfacing  BB CO1/CO3  25. 8237a DMA controller,  BB/PPT CO1/CO3  26. A/D and D/A converters  BB/PPT CO1/CO3  27. stepper motor controllers  BB/PPT CO1/CO3	1.2		T		001
14. Program writing constructs  15. Simple programs with assembler  16. Assembly language development tools  17. III 8086 interfacing, semiconductor memories  18. Memory interfacing  19. 8255 programmable peripheral interface  20. Interfacing switches and LEDS  21. Seven segment displays  22. Software and hardware interrupt applications  23. 8251 USART architecture  24. 8251 interfacing  25. 8237a DMA controller,  26. A/D and D/A converters  27. stepper motor controllers  28. Need of 8259 programmable interrupt controller  29. IV 8051 architecture, pinouts  800 Punctional building blocks of 8051  801 BB CO2  802 Data transfer and timing diagram  802 BB CO2  803 BB CO2  804 BB CO2  805 BB CO2  806 BB CO2  807 CO2  807 CO2  808 BB CO2  809 PPT CO1/CO3  809 CO2  809 PPT CO2	12.		Instruction set	BB	CO1
Simple programs with assembler  16. Assembly language development tools  17. III 8086 interfacing , semiconductor memories  18. Memory interfacing  19. 8255 programmable peripheral interface  19. BB/PPT CO1/CO3  20. Interfacing switches and LEDS  21. Seven segment displays  22. Software and hardware interrupt applications  23. 8251 USART architecture  24. 8251 interfacing  25. 8237a DMA controller,  26. A/D and D/A converters  27. stepper motor controllers  Need of 8259 programmable interrupt controller  28. Need of 8259 programmable interrupt controller  29. IV 8051 architecture, pinouts  BB/PPT CO2  Memory organization and I/O ports  BB/PPT CO2  Data transfer and timing diagram  BB CO2	13.		-	BB	CO1
Assembly language development tools  17. III 8086 interfacing , semiconductor memories  18. Memory interfacing  19. 8255 programmable peripheral interface  19. BB CO1/CO3  20. Interfacing switches and LEDS  21. Seven segment displays  22. Software and hardware interrupt applications  23. 8251 USART architecture  24. 8251 interfacing  25. 8237a DMA controller,  26. A/D and D/A converters  27. stepper motor controllers  28. Need of 8259 programmable interrupt controller  29. IV 8051 architecture, pinouts  30. Functional building blocks of 8051  31. Memory organization and I/O ports  32. Data transfer and timing diagram  BB CO1/CO3  BB CO1/CO3  BB CO1/CO3  BB CO1/CO3  CO4  BB CO2  BB CO2  BB CO2  BB CO2  CO4  BB CO2	14.		Program writing constructs	BB	CO1
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Memory interfacing   BB   CO1/CO3	16.		Assembly language development tools	BB	CO1/CO3
19.   8255 programmable peripheral interface   BB/PPT   CO1/CO3     20.   Interfacing switches and LEDS   BB   CO1/CO3     21.   Seven segment displays   BB/PPT   CO1/CO3     22.   Software and hardware interrupt applications   BB   CO1     23.   8251 USART architecture   BB/PPT   CO1     24.   8251 interfacing   BB   CO1/CO3     25.   8237a DMA controller,   BB/PPT   CO1/CO3     26.   A/D and D/A converters   BB   CO1/CO3     27.   stepper motor controllers   BB/PPT   CO1/CO3     28.   Need of 8259 programmable interrupt controller     29.   IV   8051 architecture, pinouts   BB   CO2     30.   Functional building blocks of 8051   BB   CO2     31.   Memory organization and I/O ports   BB/PPT   CO2     32.   Data transfer and timing diagram   BB   CO2     33.   CO2   CO3     34.   CO3   CO4     35.   CO4   CO5   CO5     36.   CO5   CO5   CO5     37.   CO5   CO5   CO5     38.   CO5   CO5   CO5     39.   CO5   CO5   CO5     30.   CO5   CO5   CO5     31.   CO5   CO5   CO5     32.   Data transfer and timing diagram   BB   CO5     33.   CO5   CO5   CO5     34.   CO5   CO5   CO5     35.   CO5   CO5   CO5     36.   CO5   CO5   CO5   CO5     37.   CO5   CO5   CO5   CO5     38.   CO5   CO5   CO5   CO5     39.   CO5   CO5   CO5   CO5   CO5     30.   CO5   CO5   CO5   CO5   CO5     31.   CO5   CO5   CO5   CO5   CO5     32.   CO5   CO5   CO5   CO5   CO5   CO5     33.   CO5   CO5   CO5   CO5   CO5   CO5   CO5     34.   CO5   CO5	17.	III	8086 interfacing, semiconductor memories	BB	CO1/CO3
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32. Data transfer and timing diagram BB CO2	30.		Functional building blocks of 8051	BB	CO2
	31.		Memory organization and I/O ports	BB/PPT	CO2
33. Interrupts , manipulation BB CO2	32.		Data transfer and timing diagram	BB	CO2
	33.		Interrupts, manipulation	BB	CO2
34. Control algorithms and I/O instructions BB CO2	34.		Control algorithms and I/O instructions	BB	CO2

35.		Comparison with 8085 programming	BB	CO3/CO4
36.	V	Key board and display interface programming	BB/PPT	CO3
37.		Control of servo motor application with microcontroller	PPT	CO3
38.		Control of stepper motor programming	PPT	CO3
39.		Application to automation systems	PPT	CO3
40.		Comparision of applications	BB	CO4
		Total Classes	60	

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	1										1		
CO2	3	1										1		
CO3	3	2	2	1								1	1	1
CO4	3	2	2	2								1	1	1

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# **Evaluation Pattern**

S. No	Components	Internal	External	Total
1	Theory	30	70	100
2	Engineering Graphics/Design/Drawing	30	70	100
3	Practical	15	35	50
1	Mini Project/Internship/Industrial Training/ Skill Development programmes/Research Project	-	50	50
5	Project Work	60	140	200

Marks Range Theory (Max – 100)	Marks Range Lab (Max – 50)	Level	Letter Grade	Grade Point
≥ 90	≥ 45	Outstanding	A+	10
≥80 to <89	≥40 to <44	Excellent	A	9
≥70 to <79	≥35 to <39	Very Good	В	8
≥60 to <69	≥30 to <34	Good	C	7
≥50 to <59	≥25 to <29	Fair	D	6
≥40 to <49	≥20 to <24	Satisfactory	E	5
<40	<20	Fail	F	0
-		Absent	AB	0

Day/Time	09.00-	09.50-	11.00-	11.50-	01.40-	02.30-	03.20-	04.10-
	09.50	10.40	11.50	12.40	02.30	03.20	04.10	05.00
Mon			FM&MC		FM&MC			
Tue						FM&MC		
Wed	FM&MC		FM&MC					
Thu	FM&MC		FM&MC			FM&MC		
Fri						FM&MC		
Sat	FM&MC							