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B.E. Srinivasulu

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
II/IV BE (ECE) - I Sem, Time Table, Sec - A, A.Y 2020-21

Class Teacher : Mr. P Srinivasulu

Dt: 30/10/2020
w.e.f: 02/11/2020

R-19

LH-2

Periods	1	2		3	4	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40	01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	SS	EDC / STLD LAB				STLD	EDC	RVSP	MEFA
TUE	OOPJ	STLD	BREAK	EDC	SS	RVSP	MEFA	CI	SS
WED	EDC	STLD		OOPJ	MEFA	CI	EDC / STLD LAB		
THU	CI	STLD		OOPJ	RVSP	MEFA	CI	SS	EDC
FRI	OOPJ	EDC / STLD LAB				MEFA	RVSP	OOPJ	CI
SAT	STLD	EDC		RVSP	SS				

Examinations in-charge: Mr. P. Hareesh

Attendance in-charge: Mrs. V. Swathi

Code	Subject	Staff
1	/Electronic Devices and Circuits	Mr P. Srinivasulu <i>P.S.</i>
2	Switching Theory and Logic Design	Mr. K. V. R. Chowdary
3	/Signals and Systems	Mr. H. Sudhakar <i>HS</i>
4	Random Variables and Stochastic Process	Mrs. V. Swathi <i>V. Swathi</i>
5	Object Oriented Programming Through Java	Dr. CH. Madhava Rao <i>CH</i>
6	/Managerial Economics & Financial Analysis	Mr. P. Hareesh <i>PH</i>
7	Electronic Devices and Circuits Lab	PSV / VS <i>PSV</i>
8	/Switching Theory and Logic Design Lab	NL / KVRC <i>NL</i>
9	/ Constitution of India	Mr. K. Lakshminarayana

[Signature]
Time Table I/C

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HOD-ECE
Head of the Department
Electronics & Communication Engg.
Sir C.R.R.College of Engineering
Eluru - 534 007

450
PRINCIPAL
Principal
SIR C.R.R. COLLEGE OF ENGINEERING
ELURU - 534 007

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
II/IV BE (ECE) - I Sem, Time Table, Sec - B, A.Y 2018-19
2020-21

Class Teacher : Mr. D. Ajay Kumar

Dt: 30/10/2020
w.e.f: 02/11/2020

R-19

e - Class Room

Periods Theory	1	2	BREAK	3	4	LUNCH	5	6	7	8
	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	EDC	RVSP	BREAK	STLD	OOPJ	LUNCH	SS	EDC / STLD LAB		
TUE	SS	MEFA		EDC	RVSP		STLD	CI	OOPJ	CI
WED	MEFA	EDC / STLD LAB			LUNCH	RVSP	SS	STLD	OOPJ	
THU	STLD	CI	MEFA	EDC		RVSP	OOPJ	EDC	SS	
FRI	CI	STLD	LUNCH	RVSP	OOPJ	MEFA	EDC / STLD LAB			
SAT	CI	SS		EDC	MEFA					

Examinations in-charge: Mr. Ch. JayaPrakash

Attendance in-charge: Mrs. V. Vamsisudheera

Code	Subject
1	Electronic Devices and Circuits ✓
2	Switching Theory and Logic Design ✓
3	Signals and Systems ✓
4	Random Variables and Stochastic Process ✓
5	Object Oriented Programming Through Java ✓
6	Managerial Economics & Financial Analysis ✓
7	Electronic Devices and Circuits Lab ✓
8	Switching Theory and Logic Design Lab ✓
9	Constitution of India ✓

Staff

Mr. V. Veerraju

Mr. CH. Jayaprakash

Mr. V. Vamsisudheera

Mr. A. Ramkumar

Dr. CH. Madhava Rao

Mr. D. Ajaykumar

KL / VVS

P VVR / ARK

Dr. M. Ravikumar

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Time Table I/C

Marked 20/10/20

HOD-ECE
Head of the Department
Electronics & Communication Engg.
Sir C.R.R.College of Engineering
Eluru - 534 007

40

11/02/21

PRINCIPAL
Principal
SIR C.R.R. COLLEGE OF ENG
ELURU - 534 00

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
II/IV BE (ECE) - I Sem, Time Table, Sec - C, A.Y ~~2018-19~~
2020-21

Class Teacher : Mr A. Ramkumar

Dt: 30/10/2020
w.e.f: 02/11/2020

LH-21

R-19

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12-40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	MEFA	SS		CI	RVSP		EDC	MEFA	STLD	OOPJ
TUE	RVSP	EDC / STLD LAB			SS		OOPJ	EDC	STLD	
WED	OOPJ	STLD		MEFA	OOPJ		CI	CI	MEFA	RVSP
THU	CI	EDC		RVSP	SS		STLD	EDC / STLD LAB		
FRI	EDC	SS		CI	EDC		SS	STLD	OOPJ	MEFA
SAT	RVSP	EDC / STLD LAB								

Examinations in-charge: Mr. K. Miranji

Attendance in-charge: Mr. A. Vamsi krishna

Code	Subject	Staff
1	Electronic Devices and Circuits	Mr. V. Veerraju
2	Switching Theory and Logic Design	Mr. CH. Jayaprakash
3	Signals and Systems	Mr. V. Vamsisudheera
4	Random Variables and Stochastic Process	Mr. A. Ramkumar
5	Object Oriented Programming Through Java	Mrs. K. Lavanya
6	Managerial Economics & Financial Analysis	Mr. D. Ajaykumar
7	Electronic Devices and Circuits Lab	Dr MRK / VSB
8	Switching Theory and Logic Design Lab	CHJP / AVK
9	Constitution of India	Dr. M. Ravikumar

1-2

Time Table I/C

30/10/20

HOD-ECE
 Head of the Department
 Electronics & Communication Engg.
 Sir C.R.R.College of Engineering
 Eluru - 534 007

11/11/20

PRINCIPAL
 Sir C.R.R. COLLEGE OF ENGINEERING
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SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
II/IV BE (ECE) - I Sem, Time Table, A.Y 2020-21

Ref. No: SCRREC / ECE / Time Tables (R16) / 2020-21 / 122

R16

Class Teacher : Mr A. Ramkumar

Dt: 30/10/2020

w.e.f: 02/11/2020

LH-21

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	MEFA	SS		NA	RVSP		EDC	MEFA	STLD	
TUE	RVSP	EDC / NET LAB					SS	EDC	STLD	
WED	NA	STLD		MEFA	NA		MEFA	RVSP	--	
THU	NA	EDC		RVSP	SS		STLD	EDC / NET LAB		
FRI	EDC	SS		MEFA	EDC		SS	STLD	NA	
SAT	RVSP	EDC / NET LAB								

Examinations in-charge: Mr. K. Miranji

Attendance in-charge: Mr. A. Vamsi krishna

Code	Subject	Staff
1	Electronic Devices and Circuits	Mr. V. Veerraju
2	Switching Theory and Logic Design	Mr. CH. Jayaprakash
3	Signals and Systems	Mr. V. Vamsisudheera
4	Network Analysis	Mr. T. Kranthi Kiran
5	Random Variables and Stochastic Process	Mr. A. Ramkumar
6	Managerial Economics & Financial Analysis	Mr. D. Ajaykumar
7	Electronic Devices and Circuits Lab	Dr MRK / VSB
8	Networks & Electrical Technology Lab	Mr. K. Kotaiah Chowdary

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Time Table LC

Handwritten signature
 Head of the Department
 Electronics & Communication Engg.
 Sir C. R. Reddy College of Engineering
 Eluru - 534 007

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 PRINCIPAL
 Principal
 SIR C. R. REDDY COLLEGE OF ENGINEERING
 ELURU - 534 007

Office purpose: This time table is prepared for the R16 scheme and have been rejoin in 2/4 ECE, sem-1 for the A.Y-2020-21.

1. CH. Pavan Venkata Sai Kumar - 18B81A0413
 2. E. Vankata Jaswanth - 18B81A0433
 3. B. Venkata Ramakrishna - 17B81A0410