

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
III/IV B. Tech - II Sem, Sec-A, Time Table A.Y 2022-23

Class Teacher : Mrs. K. Radha

Date : 27/01/2023
w.e.f: 30/01/2023

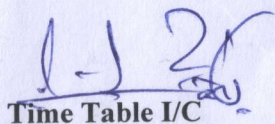
LH-22

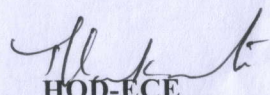
| Periods | 1 | 2 | BREAK | 3 | 4 | LUNCH | 5 | 6 | 7 | 8 |
|---------|------------|----------------|-------|-------------|-------------|-------|-----------------|-------------|-------------|-------------|
| Theory | 9.00-09.50 | 9.50-10.40 | | 11.00-11.50 | 11.50-12.40 | | 01.40-02.30 | 02.30-03.20 | 03.20-04.10 | 04.10-05.00 |
| MON | DSP | ES | | MPMC | PP | | MPMC / VLSI LAB | | - | |
| TUE | ES | VLSI | | DSP | MPMC | | AAP | PYTHON | | - |
| WED | VLSI | VLSI / DSP LAB | | | ES | | DSP | RM | - | |
| THU | ES | RM | | MPMC | VLSI | | DSP / MPMC LAB | | | - |
| FRI | PP | MPMC | | DSP | ES | | VLSI | MPMC | RM | - |
| SAT | DSP | VLSI | | AAP | | | | | | |

Examinations in-charge: Mr. A. Vamsi Krishna

Attendance in-charge: Mrs. N. Lavanya

| Code | Subject | Staff |
|------|-------------------------------------|-----------------------|
| 1 | Microprocessor and Microcontrollers | Mr. A. Vamsi Krishna |
| 2 | VLSI Design | Mrs. K. Radha |
| 3 | Digital Signal Processing | Mrs. N. Lavanya |
| 4 | Embedded System (PE-2) | Mr. R. Trinath |
| 5 | Python Programming (OE-2) | Mrs. N. Suneetha / UR |
| 6 | MP & MC Lab | AVK / HS |
| 7 | VLSI Design Lab | KR / YYP |
| 8 | Digital Signal Processing Lab | NL / VSB |
| 9 | ARM / Aurdino Programming | KM / PSV / ARK ✓ |
| 10 | Research Methodology | Dr. M. Ravikumar |


Time Table I/C


HOD-ECE
Head of the Department
Electronics & Communication Engg.
Sir C.R.R.College of Engineering
Eluru - 534 007


PRINCIPAL
Principal
Sir C.R.R.College of Engineering
ELURU - 534 007

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
III/IV B. Tech - II Sem, Sec-B, Time Table A.Y 2022-23

Class Teacher : Dr. M. Ravi Kumar

Date : 27/01/2023
w.e.f: 30/01/2023

LH-24

| Periods | 1 | 2 | | 3 | 4 | | 5 | 6 | 7 | 8 |
|------------|------------|----------------|--------------|-------------|-------------|--------------|-----------------|-------------|-------------|-------------|
| Theory | 9.00-09.50 | 9.50-10.40 | | 11.00-11.50 | 11.50-12.40 | | 01.40-02.30 | 02.30-03.20 | 03.20-04.10 | 04.10-05.00 |
| MON | MPMC | DSP / VLSI LAB | | | | LUNCH | RM | DSP | ES | - |
| TUE | ES | VLSI | BREAK | AAP | | | MPMC | DSP | PP | - |
| WED | DSP | ES | | PP | RM | | VLSI / MPMC LAB | | | - |
| THU | VLSI | MPMC | | DSP | RM | | VLSI | PYTHON | | - |
| FRI | AAP | MPMC / DSP LAB | | | | | ES | VLSI | MPMC | - |
| SAT | ES | VLSI | | MPMC | DSP | | | | | |

Examinations in-charge: Mr. V. Surendra Babu

Attendance in-charge: Mr. A. Ram Kumar

| Code | Subject | Staff |
|------|-------------------------------------|--------------------------|
| 1 | Microprocessor and Microcontrollers | Mrs. K. Radha |
| 2 | VLSI Design | Dr. P. H. S. Tejo Murthy |
| 3 | Digital Signal Processing | Mr. V. Surendra Babu |
| 4 | Embedded Systems (PE-2) | Mr. A. Ram Kumar |
| 5 | Python Programming (OE-2) | Mrs. P. Sowmithri / YY |
| 6 | MP & MC Lab | PS / Dr CHMR |
| 7 | VLSI Design Lab | Dr TM / UR |
| 8 | Digital Signal Processing Lab | VSB / Dr RS |
| 9 | ARM / Aurdino Programming | KM / PSV / DAK |
| 10 | Research Methodology | Mrs. G. Srilatha |

[Signature]
Time Table I/C

[Signature]
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
III/IV B. Tech - II Sem, Sec-C, Time Table A.Y 2022-23

Class Teacher : Mr. P. Hareesh

Date : 27/01/2023
w.e.f: 30/01/2023

LH-23

| Periods | 1 | 2 | BREAK | 3 | 4 | LUNCH | 5 | 6 | 7 | 8 |
|---------|------------|----------------|-------|-------------|-------------|-------|-----------------|-------------|-------------|-------------|
| Theory | 9.00-09.50 | 9.50-10.40 | | 11.00-11.50 | 11.50-12.40 | | 01.40-02.30 | 02.30-03.20 | 03.20-04.10 | 04.10-05.00 |
| MON | VLSI | DSP | | ES | MPMC | | RM | PYTHON | | - |
| TUE | RM | MPMC | | DSP | ES | | MPMC / VLSI LAB | | - | |
| WED | MPMC | DSP | | AAP | | | VLSI | RM | ES | - |
| THU | PP | VLSI / DSP LAB | | | MPMC | | ES | AAP | - | |
| FRI | ES | VLSI | | DSP | VLSI | | DSP / MPMC LAB | | - | |
| SAT | DSP | VLSI | | MPMC | PP | | | | | |
| | | | | | | | | | | |

Examinations in-charge: Dr. CH. Madhava Rao

Attendance in-charge: Mr. D. Ajay Kumar

| Code | Subject | Staff |
|------|-------------------------------------|---------------------|
| 1 | Microprocessor and Microcontrollers | Dr. CH. Madhava Rao |
| 2 | VLSI Design | Mr. D. Ajay Kumar |
| 3 | Digital Signal Processing | Dr. I. Hemalatha |
| 4 | Embedded Systems (PE-2) | Mr. R. Trinath |
| 5 | Python Programming (OE-2) | Dr. R. Satish / KLN |
| 6 | MP & MC Lab | Dr. CHMR / AK |
| 7 | VLSI Design Lab | DAK / KL |
| 8 | Digital Signal Processing Lab | RT / NL |
| 9 | ARM / Aurdino Programming | KM / ARK / AVK |
| 10 | Research Methodology | Mr. P. Srinivasulu |

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Time Table IC

[Signature]
HOD-ECE

Head of the Department
Electronics & Communication Engg.
Sir C.R.R.College of Engineering
Eluru - 534 007

[Signature]
PRINCIPAL

Principal
Sir C.R.R.College of Engineering
ELURU - 534 007