

**SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III/IV B. Tech - II Sem, Sec-A, Time Table A.Y 2023-24**

Class Teacher : Mrs. P. Soumithri

Date : 27/12/2023

LH-22

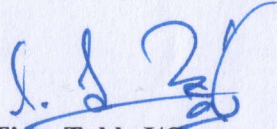
w.e.f:

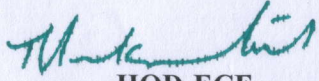
Periods	1	2		3	4		5	6	7	8	
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00	
MON	PP	VLSI LAB				LUNCH	ES	DSP	VLSI	-	
TUE	VLSI	MPMC		RM	ES		ARM	MPMC	DSP	-	
WED	ES	MPMC / DSP LAB					ES	DSP	VLSI	-	
THU	RM	DSP	BREAK	VLSI	MPMC		PP	STTP			
FRI	RM	VLSI		MPMC	ES		MPMC / DSP LAB				
SAT	MPMC	DSP		ARM							

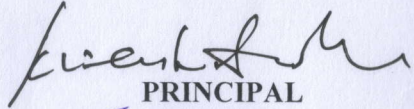
Examinations in-charge: Mrs. U. Ramya

Attendance in-charge: Mr. R. Trinath

Code	Subject	Staff
R2032041	Microprocessor and Microcontrollers	Mrs. U. Ramya
R2032042	VLSI Design	Mr. D. Ajay Kumar
R2032043	Digital Signal Processing	Dr. I. Hemalatha
R203204C	Embedded System (PE-2)	Mr. R. Trinath
R203204F	Python Programming (OE-2)	Mr. P. Sowmithri
R2032044	MP & MC Lab	UR / NSW
R2032045	VLSI Design Lab	DAK / RT / Dr TM / VSB
R2032046	Digital Signal Processing Lab	Dr IH / BVK
R2032047	ARM / Aurdino Programming	Dr KM / PSV
R2032048	Research Methodology	Dr. T. Venkateswara Rao/P.Hareesh
-	Short Term Training Program	KR/DrRS/PS/NS/KJSL
-	Internship	Dr KM / AVK / PH

  
Time Table I/C

  
HOD-ECE  
Head of the Department  
Electronics & Communication Engg.  
Sir C.R.R.College of Engineering  
Eluru - 534 007

  
PRINCIPAL  
Principal  
Sir C.R.R. College of Engineering  
ELURU - 534 007



**SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III/IV B. Tech - II Sem, Sec-B, Time Table A.Y 2023-24**

Class Teacher : Mr. V. Surendra Babu

Date : 27/12/2023

LH-23

w.e.f:

Periods	1	2		3	4		5	6	7	8	
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12-40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00	
MON	RM	MPMC / DSP				LUNCH	PP	MPMC	DSP	-	
TUE	ARM	ES	BREAK	DSP	MPMC		VLSI LAB				
WED	PP	ES		MPMC	VLSI		MPMC / DSP LAB				
THU	DSP	MPMC		ES	VLSI		DSP	ARM	-		
FRI	VLSI	DSP		RM	ES		VLSI	STTP			
SAT	MPMC	ES		VLSI	RM						

Examinations in-charge: Mr. B. Veerakumar

Attendance in-charge: Mr. A. Ram Kumar

Code	Subject	Staff
R2032041	Microprocessor and Microcontrollers	Mr. A. Vamsi Krishna
R2032042	VLSI Design	Mr. V. Surendra Babu
R2032043	Digital Signal Processing	Mr. B. Veerakumar
R203204C	Embedded Systems (PE-2)	Mr. A. Ramkumar
R203204F	Python Programming (OE-2)	Dr. R. Satish
R2032044	MP & MC Lab	AVK / HS
R2032045	VLSI Design Lab	VSB / PH / KL / DAK
R2032046	Digital Signal Processing Lab	BVK / ARK
R2032047	ARM / Aurdino Programming	DrKM/PSV
R2032048	Research Methodology	Mr. P. Hareesh
-	Short Term Training Program	KR/DrRS/PS/NS/KJSL
-	Internship	Dr KM / AVK / PH

*[Signature]*  
Time Table I/C

*[Signature]*  
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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**III/IV B. Tech - II Sem, Sec-C, Time Table A.Y 2023-24**

Class Teacher : Mrs. N. Swathi

Date : 27/12/2023

LH-26

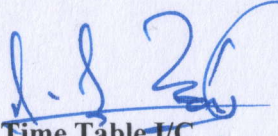
w.e.f:

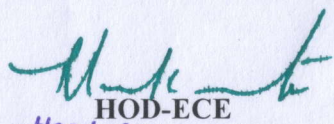
Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12-40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	RM	MPMC		DSP	ARM		MPMC / DSP LAB			-
TUE	ES	MPMC / DSP LAB					VLSI	DSP	ES	-
WED	ES	DSP		MPMC	VLSI		PP	VLSI	RM	-
THU	MPMC	VLSI LAB					PP	RM	ES	-
FRI	DSP	VLSI		ARM			MPMC	STTP		
SAT	VLSI	DSP		ES	MPMC					

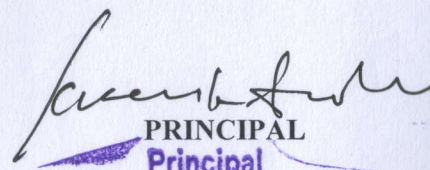
Examinations in-charge: Mrs. A. Kinnera

Attendance in-charge: Mrs. N. Lavanya

Code	Subject	Staff
R2032041	Microprocessor and Microcontrollers	Mrs. A. Kinnera
R2032042	VLSI Design	Dr. P. H. S. Tejo Murthy
R2032043	Digital Signal Processing	Mrs. N. Lavanya
R203204C	Embedded Systems (PE-2)	Mrs. N. Swathi
R203204F	Python Programming (OE-2)	Mrs. K. Radha
R2032044	MP & MC Lab	MNK / AK
R2032045	VLSI Design Lab	Dr TM/KL/PH/RT
R2032046	Digital Signal Processing Lab	NL / VSB
R2032047	ARM / Aurdino Programming	DrKM/PSV
R2032048	Research Methodology	Mr. M. Narendra Kumar
-	Short Term Training Program	KR/DrRS/PS/NS/KJSL
-	Internship	Dr KM / AVK / PH

  
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