III B. Tech I Semester Supplementary Examinations, October/November - 2020 LINEAR IC APPLICATIONS

(Common to Electronics and Communication Engineering, Electronics and Instrumentation Engineering, Electronics and Computer Engineering)

Time: 3 hours Max. Marks: 70			
	iie. 3 1	Note: 1. Question Paper consists of two parts (Part-A and Part-B) 2. Answer ALL the question in Part-A 3. Answer any FOUR Questions from Part-B	IKS. 70
		<u>PART -A</u> (14	Marks)
1.	a)b)c)d)e)f)	What is the significance of Level translator? Define Drift and give its ideal and practical values. Draw the V to I convertor. What is an all-pass filter and Notch Filter? List the applications of PLL. What are the advantages of analog to digital conversion?	[2M] [2M] [2M] [3M] [3M] [2M]
	$\underline{PART - B} \tag{56 Ma}$		
2.	a) b)	Draw the circuit diagram of differential amplifier with dual input and balanced output. Derive the expressions for differential gain A_d , input resistance R_i , and output resistance R_o Write and explain about DC coupling and cascaded differential amplifier stages.	
3.	a) b)	List out the applications and Temperature ranges of IC 741 Op-amp. List out the DC characteristics of Op-amp and Explain.	[7M] [7M]
4.	a)	For the given output expression design an adder circuit using an Op-Amp: $V_0 = -(0.1V_1 + V_2 + 10V_3)$	[7M]
	b)	With a neat sketch explain the operation of Anti log Amplifiers and derive its output voltage in detail.	[7M]
5.	a)	Design a second order butter worth low pass filter having an upper cut off frequency of 2 KHz.	
	b)	With a neat sketch explain the operation of IC 1496 balanced modulator.	[7M]
6.	a) b)	Explain the working of Astable multivibrator using 555 Timer with relevant circuits and waveforms. With a neat sketch, explain IC566 VCO operation and discuss any two	
7.	a) b)	applications. With a neat Sketch explain the R-2R ladder resistor type DAC. Define the following terms as related to ADC: i) Conversion time ii) Percentage resolution iii) Linearity.	[8M] [6M]
