Name	Dr P H S Tejo Murthy		
Designation	Professor		
Department	Electronics and Communications Engineering		
Date of Birth	25.08.1979		
Date of joining the Institution	05.06.2017		
Unique ID	1-3540246801		
	UG	PG	Ph.D.
Educational Qualifications with class/grade	B.E (ECE) First Class	M.E (Applied Electronics) First Class	Ph.D. (VLSI)
	Teaching		19 and 6 months
Work Experience	Research		-
	Industry		1 year
	Others		-
Area of Specialization	VLSI, Image processing, Artificial In		ntelligence
	UG Level		PG Level
Courses taught	VLSI,PDC,DSP,DIP,PCA		ADSP, VLSI
Research Guidance	Master's		Ph.D.
	7		
Projects carried out	One Completed[Young scientist]		
Patents	1 filed patent application numb		er-322650-001
Technology Transfer	1@ final stage		
Research Publications	International Journals		17
	National Journals		1
	International Conferences		8
	National Conferences		1
No. of Books published with details			
Achievements	Designed Explosive device and fishpond automation Gadget		