

Estd.1989

SIR C R REDDY COLLEGE OF ENGINEERING (AUTONOMOUS)

VATLURU, ELURU-534007, ELURU DISTRICT, ANDHRA PRADESH, INDIA
Approved by AICTE, Accredited by NBA(CSE, IT,ECE,EEE,ME), Permanently Affiliated to JNTUK, Kakinada
Website: www.sircrrengg.ac.in, Phone(Off): (08812)230840, Email: principal@sircrrengg.ac.in

I M.Tech. I Semester (CR25) Regular Examinations, March-2026

Time Table

Date: 27-01-2026

A.Y. : 2025 – 26

TIME: 10.00 AM TO 1.00 PM

DATE & DAY	09-03-2026 (Monday)	11-03-2026 (Wednesday)	13-03-2026 (Friday)	16-03-2026 (Monday)	18-03-2026 (Wednesday)
Machine Design (15)	Mechanical Vibrations and Acoustics (251511001)	Advanced Mechanics of Solids (251511002)	AI&ML for Mechanical Engineering (251511003)	Elective-I ➤ Advanced finite Element Methods (251511E11) ➤ Product Design & Development (251511E12) ➤ Geometric Modeling (251511E13) ➤ Numerical Methods for Mechanical Engineering (251511E14)	Elective-II ➤ Design for Manufacturing & Assembly (251511E21) ➤ Multi Body Dynamics (251511E22) ➤ Vision Systems and Image Processing (251511E23) ➤ Engineering Tribology (251511E24)

Head of the Departments are here by informed that the Laboratory external examinations for I M.Tech I Semester (CR25), to be conducted from 02-03-2026 to 07-03-2026 and submit the reports to Examination Cell on or before 09-03-2026 (Monday).

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Y. B. Arora
27/1/26
CONTROLLER OF EXAMINATIONS
Controller of Examinations

Sir C R Reddy College of Engineering (A)
ELURU-534 007

Copy to:

- The Principal's Office
- All the Department HOD's
- The coordinator of IQAC

[Signature]
PRINCIPAL
Principal
Sir C R Reddy College of Engineering
ELURU - 534 007

Prepared by
V. Gupta

verified by
m.s. pawati



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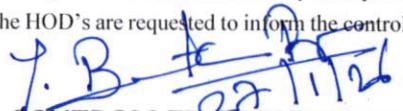
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ECE VLSI (57)	Digital CMOS Circuit Design (255711001)	Analog CMOS Circuit Design (255711002)	MoS Device Physics (255711003)	Elective-I ➤ VLSI Technology (255711E11) ➤ Nano Scale Devices (255711E12) ➤ MOS Devices Modelling and Characterization (255711E13) ➤ Digital System Design Through HDL (255711E14)	Elective-II ➤ VLSI Architectures (255711E21) ➤ Power management IC Design (255711E22) ➤ Low power Design Techniques (255711E23) ➤ CAD for VLSI (255711E24)

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Prepared by
V. G. Reddy

Verified by
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CSE Computer Science & Technology (59)	Advanced Algorithms Analysis (255911001)	Advanced Data warehousing and Data mining (255911002)	Artificial Intelligence (255911003)	Elective – I ➤ Image processing (255911E11) ➤ Soft Computing (255911E12) ➤ Advanced Computer Networks (255911E13) ➤ Advanced Software Engineering (255911E14)	Elective – II ➤ Time Series Analysis (255911E21) ➤ High Performance Computing (255911E22) ➤ Agile Methodologies (255911E23) ➤ Advanced Compiler Design (255911E24)

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Y. B. Reddy
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Prepared by
V. G. Reddy

verified BY
MSR



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EEE Power Systems and Automation (66)	Power System Operation and Control (256611001)	Smart Grid Technologies (256611002)	Reactive Power Compensation and Management (256611003)	Elective-I ➤ Electrical Distribution Automation (256611E11) ➤ Advanced Power Systems Protection (256611E12) ➤ Electric Vehicles (256611E13)	Elective-II ➤ HVDC Transmission (256611E21) ➤ Power Electronic Converters (256611E22) ➤ Programmable Logic Controllers and Applications (256611E23)

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Prepared by
v. Lakshmi

verified by
m.s.p