


<b>Name</b>	Ajaykumar Dharmireddy		
<b>Designation</b>	Assistant Professor		
<b>Department</b>	E.C.E		
<b>Date of Birth</b>	30-03-1984		
<b>Date of joining the Institution</b>	19-06-2010		
<b>Unique ID</b>	1-435604690		
<b>Educational Qualifications with class/grade</b>	<b>UG</b>	<b>PG</b>	<b>Ph.D</b>
	B.Tech (E.C.E)	M.E (VLSI Design)	Perusing
	First Class	First Class	
<b>Work Experience</b>	<b>Teaching</b>		15
	<b>Research</b>		5
	<b>Industry</b>		1
	<b>Others</b>		--
<b>Area of Specialization</b>	<b>VLSI Design</b>		
<b>Courses taught</b>	<b>UG Level</b>		<b>PG Level</b>
	12		08
<b>Research Guidance</b>	<b>Master's</b>		<b>Ph.D.</b>
	10		-Nil-
<b>Projects carried out</b>	-Nil-		
<b>Patents</b>	01("Live start-up circuit for band gap Reference (BGR)",		
<b>Technology Transfer</b>	<b>H-spice design tools.</b>		
<b>Research Publications</b>	<b>International Journals</b>		32
	<b>National Journals</b>		04
	<b>International Conferences</b>		08
	<b>National Conferences</b>		05
<b>No. of Books published with details</b>	Book chapter ("Block chain Technology for IoT and Wireless Communications" by CRC press).		
<b>Achievements</b>	Ratified as Assistant Professor from Andhra University Ratified as Assistant Professor from JNT University, Kakinada.		