


<b>Name</b>	Pancheti Hareesh		
<b>Designation</b>	Assistant Professor		
<b>Department</b>	Electronics and Communication Engineering		
<b>Date of Birth</b>	17 <sup>th</sup> August 1987		
<b>Date of joining the Institution</b>	06 <sup>th</sup> September 2010.		
<b>Unique ID</b>	1-435604814		
<b>Educational Qualifications with class/grade</b>	<b>UG</b>	<b>PG</b>	<b>Ph.D</b>
	B.Tech. (ECE)	M.Tech. (VLSI Design)	Pursuing ECE-MEMS
	Second Class	First Class	
<b>Work Experience</b>	<b>Teaching</b>		14 years
	<b>Research</b>		5 years
	<b>Industry</b>		Nil
	<b>Others</b>		Nil
<b>Area of Specialization</b>	M.Tech-VLSI Design, Ph.D-MEMS		
<b>Courses taught</b>	<b>UG Level</b>		<b>PG Level</b>
	STLD,DICD,VLSID,MEFA,MS		VLSID,LPVD
<b>Research Guidance</b>	<b>Master's</b>		<b>Ph.D.</b>
	13		Nil
<b>Projects carried out</b>	Nil		
<b>Patents</b>	Nil		
<b>Technology Transfer</b>			
<b>Research Publications</b>	<b>International Journals</b>		14
	<b>National Journals</b>		0
	<b>International Conferences</b>		3
	<b>National Conferences</b>		0
<b>No. of Books published with details</b>	Nil		
<b>Achievements</b>	Nil		