

Name	Dr. CH JAYAPRAKASH				
Designation	Associate Professor				
Department	ECE				
Date of Birth	10 MARCH 1983				
Date of joining the Institution	14/05/2011				
Unique ID	1-718038351				
Educational Qualifications with class/grade	UG	PG	AICTE QIP PG	Ph.D.	
	B.E (ECE)	M.Tech (VLSI SYSTEM DESIGN)	Robotics & Automation	Completed 2024	
	First Class	First Class	9.39		
Work Experience	Teaching				19
	Research				0
	Industry				0
	Others				0
Area of Specialization	VLSI ,MEMS				
Courses taught	UG Level			PG Level	
	Electronic devices and circuit, switching theory logic circuits,micro processor and micro controllers, linear ic application's ,digital ic application			Digiatl systems design,MEMS,Desi gn for testability, Digiatl systems design hardware decription language	
Research Guidance	Master's			Ph.D.	
	7			--	
Projects carried out	--				
Patents	1. Context-aware depth and RGB data fusion using deep attention networks for robust human action recognition, 06/20/25, 202541055789 A 2. AI-augmented energy optimization in wireless IoT sensor networks via hybridized learning models and dynamic topology control, 02/07/25, 202541006534 A3. Quantified analysis and classified detection of volatile organic compounds with MEMS microcantilever array, 202341086627, 12/18/23. 4. Novel optimized RF MEMS switch for satellite and wireless communication systems, 202341080119, 11/24/234				
Technology Transfer	--				
Research Publications	International Journals			2-SCI,7-SCOPUS	
	National Journals			--	
	International Conferences			--	
	National Conferences			--	
No. of Books published with details	--				