

Name	Dr.Ajay Kumar Dharmireddy		
Designation	Associate Professor		
Department	E.C.E		
Date of Birth	30-03-1984		
Date of joining the Institution	19-06-2010		
Unique ID	1-435604690		
Educational Qualifications	UG	PG	Ph.D.
	B.Tech (ECE)	M.Tech (VLSI D)	Device Modeling
	First Class	First Class	
Work Experience	Teaching		16
	Research		05
	Industry		6 months
	Others		
Area of Specialization	VLSI Design		
Courses taught	UG Level		PG Level
	VLSI Design & Embedded system Microprocessors TV and Satellite Communication Probability theory & Random process Electronic device & circuits. Basic Electronics Ethics & Moral values Management Science UG Management & organization ctober . Principles of Economics and management. Electronic-I System model & simulation. Embedded System		VLSI design techniques Wavelet Transforms and applications Satellite communication & propagation array Digital system testing & testable design. VHDL modeling of digital systems.Business Analytics Physical design Automation Device Modeling
Research Guidance	Master's		Ph.D.
	10		--
Projects carried out	--		
Patents	Live start-up circuit for band gap Reference BGR, patent published. Patent filed : Application Ref Number- 202041052318A, publication date :11-12-2020, Indian Patent Office. ARTIFICIAL INTELLIGENCE BASED SYSTEM FOR REMOTE HEALTH MONITORING OF ELDERLY PATIENTS, patent published. Patent filed : Design No.:420486-001, Filing Date- :13/09/2024 –Indian Patent Office.		
Technology Transfer	--		
Research Publications	International Journals		08
	National Journals		20
	International Conferences		18
	National Conferences		02
No. of Books published with details	Device Modeling and Characterisation of Hybrid devices Ajay kumar Dharmireddy, Sreenivasa Rao Ijjada ,LAMBERT ACADAMIC PublishingBook ;ISBN: 978-620-8-22531-5. 2024		