


<b>Name</b>	CH JAYAPRAKASH			
<b>Designation</b>	ASSISTANT PROFESSOR			
<b>Department</b>	ECE			
<b>Date of Birth</b>	10 MARCH 1983			
<b>Date of joining the Institution</b>	14/05/2011			
<b>Unique ID</b>	1-718038351			
<b>Educational Qualifications with class/grade</b>	<b>UG</b>	<b>PG</b>	<b>Ph.D.</b>	
	B.E (ECE)	M.Tech (VLSI SYSTEM DESIGN)	Perusing (MEMS)	
	First Class	First Class		
<b>Work Experience</b>	<b>Teaching</b>			17
	<b>Research</b>			0
	<b>Industry</b>			0
	<b>Others</b>			0
<b>Area of Specialization</b>	VLSI ,MEMS			
<b>Courses taught</b>	<b>UG Level</b>		<b>PG Level</b>	
	EDC,ECA,STLD,MP&MC,LICA,PDC,S S,VLSI,DICA,PDC,DICD,DICDCMOS,AICD		SMS,DSD,MEMS,DF T	
<b>Research Guidance</b>	<b>Master's</b>			<b>Ph.D.</b>
	5			--
<b>Projects carried out</b>	--			
<b>Patents</b>	2			
<b>Technology Transfer</b>	--			
<b>Research Publications</b>	<b>International Journals</b>			8
	<b>National Journals</b>			--
	<b>International Conferences</b>			--
	<b>National Conferences</b>			--
<b>No. of Books published with details</b>	--			
<b>Achievements</b>				