

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
II/IV BE (ECE) - I Sem, Time Table, Sec - A, A.Y 2018-19

Class Teacher : Mr. A. Ram Kumar

Dt: 14/06/2018
w.e.f: 18/06/2018

LH-4

| Periods Theory | 1 9.00-09.50 | 2 9.50-10.40 | | 3 11.00-11.50 | 4 11.50-12.40 | | 5 01.40-02.30 | 6 02.30-03.20 | 7 03.20-04.10 | 8 04.10-05.00 | |
|----------------|-----------------|-----------------|-------|------------------|------------------|-------|-----------------------------|-------------------------|------------------|------------------|------------------|
| MON | STLD | RVSP | | MEFA | EDC | LUNCH | RVSP | NA(T) LH-4 & ECE - I | SS(T) | Counseling | |
| TUE | SS | EDC/NET LAB | | | | | MEFA(T) LH - 4 & ECE - I | EDC(T) | RVSP | NA | |
| WED | EDC | MEFA | BREAK | STLD | RVSP | | SS | EDC/NET LAB | | | |
| THU | SS | EDC | | NA | EDC | | STLD | MEFA | RVSP(T) | STLD(T) | LH - 4 & DIC Lab |
| FRI | RVSP | NA | | MEFA | SS | | NA | STLD | EDC | | |
| SAT | NA | STLD | | MEFA | SS | | | | | | |

Examinations in-charge: Mr. P.Srinivasulu

Attendance in-charge: Mr. K. Chandrasekhar

| Code | Subject | Staff |
|------|---|----------------------|
| 1 | Electronic Devices and Circuits | Mr K. Miranji |
| 2 | Switching Theory and Logic Design | Mr. V. Veerajju |
| 3 | Signals and Systems | Mr. Ch. Jaya Prakash |
| 4 | Network Analysis | Mr. T kranthi kiran |
| 5 | Random Variables and Stochastic Process | Mr. A. Ram Kumar |
| 6 | Managerial Economics & Financial Analysis | Mr. K. Chandrasekhar |
| 7 | Electronic Devices and Circuits Lab | VVR/PSV/AVK/KCS |
| 8 | Networks & Electrical Technology Lab | TKK |

[Signature]
Time Table I/C

[Signature]
HOD-ECE
Head of the Department
Electronics & Communication Engg
Sir C.R.R.College of Engineering
Eluru - 534 007

[Signature]
PRINCIPAL
Principal
SIR C.R.R. COLLEGE OF ENGINEERING
ELURU - 534 007.

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
II/IV BE (ECE) - I Sem, Time Table, Sec - B, A.Y 2018-19

Class Teacher : Mrs G. Srilatha

Dt: 14/06/2018

w.e.f: 18/06/2018

e - Class Room

| Periods | 1 | 2 | BREAK | 3 | 4 | LUNCH | 5 | 6 | 7 | 8 |
|---------|----------------------------------|-------------|-------|-----------------------------------|-------------|-------|-------------|-------------|---------------------------------|-------------|
| Theory | 9.00-09.50 | 9.50-10.40 | | 11.00-11.50 | 11.50-12-40 | | 01.40-02.30 | 02.30-03.20 | 03.20-04.10 | 04.10-05.00 |
| MON | NA | EDC | BREAK | RVSP | STLD | LUNCH | SS | MEFA | EDC | |
| TUE | NA | RVSP | | SS | RVSP | | MEFA | EDC | SS(T) e- class room & ECE -I | STLD(T) |
| WED | STLD | EDC/NET LAB | | | NA | | EDC | SS | RVSP | |
| THU | RVSP | STLD | BREAK | SS | NA | | MEFA | EDC/NET LAB | | |
| FRI | MEFA | STLD | | RVSP(T) e- class room & DICLab | NA(T) | | EDC | SS | MEFA | Counseling |
| SAT | EDC(T) e- class room & DICLab | MEFA(T) | | NA | STLD | | | | | |

Examinations in-charge: Mr. Ch. Jaya Prakash

Attendance in-charge: Mr. V. Veerraju

| Code | Subject | Staff |
|------|---|----------------------|
| 1 | Electronic Devices and Circuits | Mrs G. Srilatha |
| 2 | Switching Theory and Logic Design | Mr. V. Veerraju |
| 3 | Signals and Systems | Mr. Ch. Jaya Prakash |
| 4 | Network Analysis | Mr. T kranthi kiran |
| 5 | Random Variables and Stochastic Process | Mr. A. Ram Kumar |
| 6 | Managerial Economics & Financial Analysis | Mr. P. Hareesh |
| 7 | Electronic Devices and Circuits Lab | HS/GS/ CHJP |
| 8 | Networks & Electrical Technology Lab | SNVB |

[Signature]
Time Table /e

[Signature]
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Head of the Department
Electronics & Communication Engg.
Sir C.R.R.College of Engineering
Eluru - 534 007

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PRINCIPAL
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ELURU - 534 007.

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
II/IV BE (ECE) - I Sem, Time Table, Sec - C, A.Y 2018-19

Class Teacher : Mr M N Venkatesh Babu

Dt: 14/06/2018
w.e.f: 18/06/2018

LH-21

| Periods | 1 | 2 | | 3 | 4 | | 5 | 6 | 7 | 8 |
|---------|------------|-------------|-------|-------------|-------------|-------|-----------------------------|------------------------------|-------------|-------------|
| Theory | 9.00-09.50 | 9.50-10.40 | | 11.00-11.50 | 11.50-12.40 | | 01.40-02.30 | 02.30-03.20 | 03.20-04.10 | 04.10-05.00 |
| MON | NA | SS | BREAK | MEFA | STLD | LUNCH | RVSP | EDC/NET LAB | | |
| TUE | EDC | MEFA | | SS | EDC | | SS | RVSP(T) LH - 21 & DIC Lab | NA(T) | STLD |
| WED | RVSP | NA | | RVSP | EDC | | SS (T) LH - 21 & DIC Lab | STLD(T) | NA | MEFA |
| THU | SS | RVSP | | STLD | NA | | RVSP | EDC(T) LH - 21 & ECE - 1 | MEFA(T) | Counseling |
| FRI | MEFA | EDC/NET LAB | | | EDC | | NA | STLD | | |
| SAT | STLD | SS | | EDC | MEFA | | | | | |

Examinations in-charge: Mr. K. Miranji

Attendance in-charge: Mr. A. Vamsi krishna

| Code | Subject | Staff |
|------|---|-----------------------|
| 1 | Electronic Devices and Circuits | Mr K. Miranji |
| 2 | Switching Theory and Logic Design | Mr. A. Vamsi krishna |
| 3 | Signals and Systems ✓ | Mr M N Venkatesh Babu |
| 4 | Network Analysis | Mr. A Durga Prasad |
| 5 | Random Variables and Stochastic Process | Mrs P. Sasi Bala |
| 6 | Managerial Economics & Financial Analysis | Mrs P. Sowmithri |
| 7 | Electronic Devices and Circuits Lab | KM/AVK/KL |
| 8 | Networks & Electrical Technology Lab | ADP |

(Signature)
Time Table I/C

(Signature)
HOD-ECE
Head of the Department
Electronics & Communication Engg.
Sir C.R.R.College of Engineering
Eluru - 534 007

(Signature)
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Principal
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