

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-7A1:L224
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
IV/IV B. Tech (ECE) -II Sem, Time Table, Sec - A, A.Y 2018-19
COORDINATOR: Dr. P.H.S.Tejomurthy

LH- 4

Dt: 17-11-2018

w.e.f: 19-11-2018

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	VLSI (CHJP)			VLSI (YB)			PDC	EMF	DC	
TUE	VLSI (DAK)			VLSI (PH)			PTRP	DIP	AEC	
WED	VLSI (Dr.TM)			VLSI (NSW)			STLD	EDC	SS	
THU	PROJECT WORK/GATE									
FRI	PROJECT WORK/GATE									
SAT	PROJECT WORK/GATE									

Backlog Subjects

Pulse and Digital Circuits
 EMF Theory
 Digital Communications
 Probability Theory & Random Process
 Digital Image Processing
 Analog Electronic Circuits
 Switching Theory & Logic Design
 Electronic Devices & Circuits
 Signal & Systems

Staff

Mr. H.Sudhakar
 Mr. M.N.Venkatesh Babu.S
 Ch.Ravi Kumar
 Mr. A.Ram Kumar
 Dr.P.H.S.Tejomurthy
 Mr. V.Veerraju
 Mr. Ch.Jaya Prakesh
 Dr. T.Venkateswara Rao
 Ms. V.Vamsi Sudheera


 Time Table I/C


HOD - ECE
 Head of the Department
 Electronics & Communication Engg.
 Sir C.R.R.College of Engineering
 Eluru - 534 007


Principal
Principal
 SIR C.R.R. COLLEGE OF ENGINEERING
 ELURU - 534 007.

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-7
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
IV/IV B. Tech (ECE) -II Sem, Time Table, Sec - B, A.Y 2018-19
COORDINATOR: Mr. K.Miranji

ECE-1

Dt: 17-11-2018
w.e.f: 19-11-2018


Periods	1	2	BREAK		3	4	LUNCH						
Theory	9.00-09.50	9.50-10.40			11.00-11.50	11.50-12.40							
							5	6	7	8			
MON	ES(VRK)				ES(KL)		PDC	EMF	DC				
TUE	ES(VS)				ES(PSV)		PTRP	DIP	AEC				
WED	ES(KM)				ES(AVK)		STLD	EDC	SS				
THU	PROJECT WORK/GATE												
FRI	PROJECT WORK/GATE												
SAT	PROJECT WORK/GATE												

Backlog Subjects

Pulse and Digital Circuits
EMF Theory
Digital Communications
Probability Theory & Random Process
Digital Image Processing
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Switching Theory & Logic Design
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Time Table I/C


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SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-7
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
IV/IV B. Tech (ECE) -II Sem, Time Table, Sec - C, A.Y 2018-19
COORDINATOR: Dr. T.Venkateswara Rao / KJSL

Dt: 17-11-2018
w.e.f: 19-11-2018

E-Class Room

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	RADAR & WIRELESS(GS)			RADAR & WIRELESS(MN)			PDC	EMF	DC	
TUE	RADAR & WIRELESS(GS)			RADAR & WIRELESS(MN)			PTRP	DIP	AEC	
WED	RADAR & WIRELESS(KCS)			RADAR & WIRELESS(VS)			STLD	EDC	SS	
THU	PROJECT WORK/GATE									
FRI	PROJECT WORK/GATE									
SAT	PROJECT WORK/GATE									

Backlog Subjects

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- EMF Theory
- Digital Communications
- Probability Theory & Random Process
- Digital Image Processing
- Analog Electronic Circuits
- Switching Theory & Logic Design
- Electronic Devices & Circuits
- Signal & Systems

Staff

- Mr. H.Sudhakar
- Mr. M.N.Venkatesh Babu.S
- Ch.Ravi Kumar
- Mr. A.Ram Kumar
- Dr.P.H.S.Tejomurthy
- Mr. V.Veerraju
- Mr. Ch.Jaya Prakesh
- Dr. T.Venkateswara Rao
- Ms. V.Vamsi Sudheera

[Signature]
Time Table I/C

[Signature]
HOD - ECE
Head of the Department
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Eluru - 534 007

[Signature]
Principal
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SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-7
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
IV/IV B. Tech (ECE) -II Sem, Time Table, Sec - A, A.Y 2018-19
COORDINATOR: Ms. I.Hemalatha

LH- 4

Dt: 17-11-2018
w.e.f: 19-11-2018

Periods	1	2		3	4		5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	PROJECT WORK/GATE					LUNCH				
TUE	PROJECT WORK/GATE									
WED	PROJECT WORK/GATE									
THU	CS(KLN)	BREAK		CS(CHRK)			PDC	EMF	DC	
FRI	CS(KLN)			CS(CHMR)			PTRP	DIP	AEC	
SAT	CS(IH)			CS(MNK)						

Backlog Subjects

Pulse and Digital Circuits
EMF Theory
Digital Communications
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Analog Electronic Circuits
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Electronic Devices & Circuits
Signal & Systems

Staff

Mr. H.Sudhakar
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Time Table I/C


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Eluru - 534 007


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ELURU - 534 007.

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-7
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
IV/IV B. Tech (ECE) -II Sem, Time Table, Sec - A, A.Y 2018-19
COORDINATOR: Mr. R.Satish

ECE-1

Dt: 17-11-2018
w.e.f: 19-11-2018

Periods	1	2		3	4		5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	PROJECT WORK/GATE					LUNCH				
TUE	PROJECT WORK/GATE									
WED	PROJECT WORK/GATE									
THU	DIP(VSB)	BREAK		DIP(SKR)			PDC	EMF	DC	
FRI	DIP(RS)			DIP(PSB)			PTRP	DIP	AEC	
SAT	DIP(PSB)			DIP(NL)						

Backlog Subjects

Pulse and Digital Circuits
EMF Theory
Digital Communications
Probability Theory & Random Process
Digital Image Processing
Analog Electronic Circuits
Switching Theory & Logic Design
Electronic Devices & Circuits
Signal & Systems

Staff

Mr. H.Sudhakar
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Mr. V.Veerraju
Mr. Ch.Jaya Prakesh
Dr. T.Venkateswara Rao
Ms. V.Vamsi Sudheera


Time Table i/C


HOD - ECE
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Electronics & Communication Engg
Sir C.R.R.College of Engineering,
Eluru - 534 007


Principal
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SIR C.R.R. COLLEGE OF ENGINEERING
ELURU - 534 007.

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-7
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
IV/IV B. Tech (ECE) -II Sem, Time Table, Sec - A, A.Y 2018-19
COORDINATOR: Mr. M.Ravi Kumar

E-Class Rom

Dt: 17-11-2018

w.e.f: 19-11-2018

Periods	1	2		3	4		5	6	7	8
Theory	9.00-9.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	PROJECT WORK/GATE					LUNCH				
TUE	PROJECT WORK/GATE									
WED	PROJECT WORK/GATE									
THU	DSP & BIO-MEDICAL(MR)	BREAK		DSP & BIO-MEDICAL(HS)			PDC	EMF	DC	
FRI	DSP & BIO-MEDICAL(MG)			DSP & BIO-MEDICAL(ARK)			PTRP	DIP	AEC	
SAT	DSP & BIO-MEDICAL(VVS)			DSP & BIO-MEDICAL(MR)						

Backlog Subjects

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Staff

- Mr. H.Sudhakar
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- Mr. V.Veeraraju
- Mr. Ch.Jaya Prakesh
- Dr. T.Venkateswara Rao
- Ms. V.Vamsi Sudheera


Time Table I/C


HOD - ECE
HEAD OF THE DEPARTMENT
Electronics & Communication Engg
Sir C.R.R.College of Engineering
Eluru - 534 007


Principal
Principal
SIR C.R.R. COLLEGE OF ENGINEERING
ELURU - 534 007.

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU-07
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
III/IV B. Tech (ECE-A/s) - II Sem, Time Table, A.Y: 2018-19

Class Teacher : CH. MADHAVA RAO

Dt: 14/11/2018
w.e.f: 19/11/2018

LH-27

Periods	1	2	BREAK	3	4	LUNCH	5	6	7	8
Theory	9.00-09.50	9.50-10.40		11.00-11.50	11.50-12.40		01.40-02.30	02.30-03.20	03.20-04.10	04.10-05.00
MON	MP	DSP/MP LAB				DIP	MOOCS			
TUE	DIP	MP	BREAK	CNE	CMC	EMI	SDP			
WED	CNE	DC		CMC(T)	EMI(T)	DSP/MP LAB			X	
THU	EMI	DIP		DC	MP	CNE	SDP			
FRI	MP	CMC		DC	EMI	SDP		COUSSELLING		
SAT	DC	DIP		CMC	CNE					

Examinations in-charge: Sk. Raheemunnisa

Attendance in-charge: K.Chandra Sekhar

Code	Subject	Staff
3201	Computer Network Engineering	Ch. Madhava Rao
3202	Micro processor & Micro controllers	K. Radha
3203	Digital Communications	P. Sowmithri
3204	Electronic Measurements & instrumentation	M. Greeshma
3205	Cellular and Mobile Communication	K. Chandra Sekhar
3206	Digital Image Processing	SK. Raheemunnisa
3207	DSP with MATLAB Lab	SKR / KCS
3208	Micro processor & Micro controllers Lab	CHMR / MG
	Student development Programme(SDP)	MG/KCS/SKR
	MOOCS	CHMR

Time Table /C

Handwritten Signature
HOD-ECE
Head of the Department
Electronics & Communication Engg
Sir C.R.R.College of Engineering
Eluru - 534 007

Handwritten Signature
PRINCIPAL

Handwritten Signature
Principal
SIR C.R.R. COLLEGE OF ENGINEERING
ELURU - 534 007.