

SIR C. R. REDDY COLLEGE OF ENGINEERING, ELURU

DT:- 10-08-2024.

Minutes of first BOS meeting A.Y.: 2024-25.

Subject:- Ist B.Tech, ECE - B.E.E.E and Ist M.Tech (VLSI Design)
CR24-Theory and Labs.

Date:- 10-08-2024.

Venue:- Department office of E.C.E.

Schedule:- 10/08/2024, Saturday 11:30 A.M.

Mode of meeting:- online meeting (zoom).

Agenda:-

- * Introduction of BOS members.
- * To discuss and finalize the proposed BEEE subject for Ist year B.Tech (Theory) course of CR-24 regulations.
- * To discuss and finalize the proposed Ist M.Tech Ist & IInd sem, VLSI design (Theory and labs) courses of CR-24 Regulation.
- * Finalization of model paper and list of paper setters.
- * To finalize the evaluation procedure for continuous internal evaluation (CIE) and semester end evaluation (SEE).
- * Ratification of course objectives and course outcomes, CO-PO mapping for the proposed subjects.
- * Finalization of Textbooks and Reference Text books.
- * Any other item with the permission of the chairman.

Resolution

✓ Resolution on 1st year B.Tech B.E.E.E subject :-

* Member of Academic Council Dr. P. Sreehari Rao suggested the change in Basic electronics in E.C.E Part only.

✓ Resolution on 1st M.Tech Ist & IInd sem, VLSI design subjects (Theory & Labs) :-

* CMOS Analog IC design :- Member of academic Council Dr. P. Sreehari Rao suggested remove Vth unit (Comparators) and IVth unit maybe divided into two parts. As IVth unit on OP-Amp and Vth unit as bandgap reference.

* VLSI Technology :- Since unit IV & V are not relevant to titles, add multigate MOSFET and FinFET technology and also add chapter Vth processors from Sze textbook. Suggested by Dr. P. Sreehari Rao garu. Also suggested as Sze textbook as reference.

* Device modeling :- suggested text book Nandita Dasgupta "Semiconductor devices: Modeling and technology" PHI Learning Pvt Ltd, 2004. and Yannis Tsividis "Mixed analog - digital VLSI device & technology" world scientific publisher, 2002 by Dr. P. Sreehari Rao garu.

* Research Methodology & IPR :- To add government support for technology transfer in the chapter Vth suggested by Dr. K. Murali Krishna garu.

* CMOS Analog IC design lab :- Experiment 10 & 11 maybe removed and keep in the mixed signal IC design lab

* Member of academic council DR P. Sreehari Rao garu suggested add the name of the experiments are: Bandgap references, Design of ALU, memory and system design and also suggested conduct eight weeks - hands on session. suggested 'Cadence' software by DR P. Sreehari Rao garu.

* Mixed signal & RF IC design :- Need to change the chapter titles in this subject suggested by DR P. Sreehari Rao. The suggested names are: 1st chapter: Switching capacitors. 2nd chapter: Comparators; 3rd chapter: Data converters; 4th chapter: PLL & oscillators; 5th chapter: RF & LNA. and suggested textbook RF & Microelectronics by Behzad Razavi.

* Low power VLSI design :- suggested text book Kaushik Roy "Low power CMOS VLSI circuits design" Wiley india Ltd, 2009 by DR P. Sreehari Rao garu.

* Mixed signal & RF IC design lab :- Add the 10th and 11th experiments from CMOS analog IC design lab experiments and also add high speed & high gain comparators experiment suggested by DR P. Sreehari Rao garu.

Membels presented

	Name of Members	Designation	Signature
		Chairperson	
①	DR. P. H. S. Tejomurthy	Prof & HOD of ECE	PASS ✓
②	DR. N. Balaji	Member Prof & HOD, JNTUK	NW. online
③	DR. Puli Kishore	Member, Professor NIT, T.P. Guduru.	online.
④	DR. P. Sreehari Rao	Member, Professor NIT, Warangal.	online.
⑤	DR. Murali Krishna	Member, Industrial expert. Mechatronics, MSKE	offline

Name of members

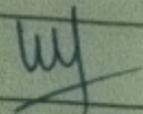
Designation

Signature

(6)

DR. M. Ravi Kumar

member.
Assoc. Professor
SINCRROE



(7)

DR. Chitalka Mahesh

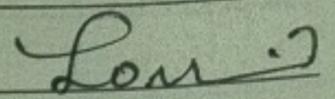
Member
Director, Airport of India.

online.

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DR. K. J. S. Loosraini.

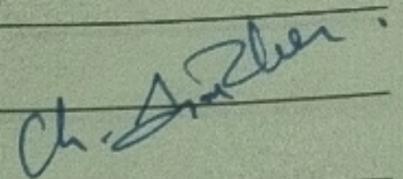
Member
Assoc. Professor,
SINCRROE.



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DR. Ch. Jaya Prakash

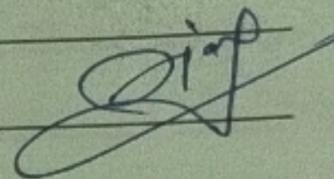
Member
Assoc. Professor
SINCRROE.



(10)

D. Ajay Kumar

Member, M-Tech co-ordinator
Asst. Professor, SINCRROE



PAST

HOD of ECE.

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